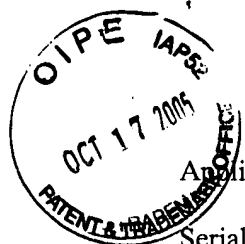


IFW



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Juan-Jann Jou, et al.	§	Docket No.:	24061.80 (TSMC2003-0343)
Serial No.:	10/772,736	§	Examiner:	David S. Blum
Filed:	February 5, 2004	§	Art Unit:	2813
For:	Interconnect Structure and Method for its Fabricating	§	Conf. No.:	5636

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents
Mail Stop: Amendment
PO Box 1450
Alexandria, VA 22313-1450

Dear Sir:

The present paper is being submitted in response to the Restriction Requirement Office Action dated September 12, 2005 in the above-identified application.

Election of Claims begins on page 2 of this paper.

Amendments to the Claims are reflected in the Listing of Claims which begins on page 3 of this paper.

Remarks begin on page 10 of this paper.

I. Election

In the Office Action mailed September 12, 2005, the Examiner alleges that the application contains claims directed to two inventions and, thus, required restriction of either:

Group I: Claims 1-26, drawn to a method of making a semiconductor device; or

Group II: Claims 27-51, drawn to a semiconductor device.

In response, Applicants hereby elect Group II, corresponding to claims 27-51. Applicants' election is made with traverse on the grounds that the embodiments delineated by the Examiner are not patentably distinct and therefore constitute a single invention concept.